

TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



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SPECIFICATION

MODULE NO.: WF121FSYAPLNB0#

General Specifications

Item	Dimension	Unit
Size	12.1	inch
Dot Matrix	1280 x RGB. x 800	dots
Module dimension	308.0 x 214.1 x 13.0	mm
Active area	261.12 x 163.2	mm
Pixel pitch	0.204 x 0.204	mm
LCD type	TFT, Normally Black, Transmissive	
Backlight Type	LED ,Normally White	
Viewing Angle	85/85/85/85	
TFT Interface	LVDS	
Aspect Ratio	16:9	
PCAP Driver IC	ILI2132 or equivalent	
PCAP Resolution	16384*16384	
PCAP Interface	USB (I2C available)	
PCAP FW Version	0x07.0x00.0x00.0x00.0x02.0x00.0x00.0x00	
Touch Panel	With PCAP	
Surface Treatment	Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

1. Typical Operation Conditions

Items	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	VDD	3.0	3.3	3.6	V
Supply current	IDD	-	340	510	mA
Logic Low input voltage	VIH	0	-	0.2*VDD	V
Logic High input voltage	VIL	0.8*VDD	-	VDD	V
Logic Low output voltage	VOH	-	-	GND+0.4	V
Logic High output voltage	VOL	DVDD-0.4	-	-	V
Supply CTP	VDDT(3.3V)	3.0	3.3	3.6	I2C type
	IDDT(3.3V)	-	87	135	
	VDDT(5V)	-	5.0	-	USB type
	IDDT(5V)	-	92	140	

Backlight Driving Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
LED current	ILED	-	360	-	mA
LED voltage	VLED+	22.0	24.0	26.0	V
LED Life Time		-	50,000	-	Hr

Interface

1. LCM PIN Definition

Pin No.	Symbol	Function
1	NC	No contact
2	VDD	Power supply VDDIN=3.3V
3	VDD	
4	VDD	
5	NC	No contact
6	GND	Digital power
7	GND	Digital ground
8	RIN0-	LVDS Negative data signal (-)
9	RIN0+	LVDS Negative data signal (+)
10	GND	GND
11	RIN1-	LVDS Negative data signal (-)
12	RIN1+	LVDS Negative data signal (+)
13	GND	GND
14	RIN2-	LVDS Negative data signal (-)
15	RIN2+	LVDS Negative data signal (+)
16	GND	GND
17	LVDS_CLK-	LVDS Negative CLK signal (-)
18	LVDS_CLK+	LVDS Negative CLK signal (+)
19	GND	GND
20	RIN3-	LVDS Negative data signal (-)
21	RIN3+	LVDS Negative data signal (+)
22	GND	GND
23	NC	No contact
24	NC	No contact
25	GND	GND
26	NC	No contact
27	NC	No contact
28	GND	GND
29	NC	No contact
30	NC	No contact
31	GND	GND

32	GND	GND
33	GND	GND
34-40	NC	No contact

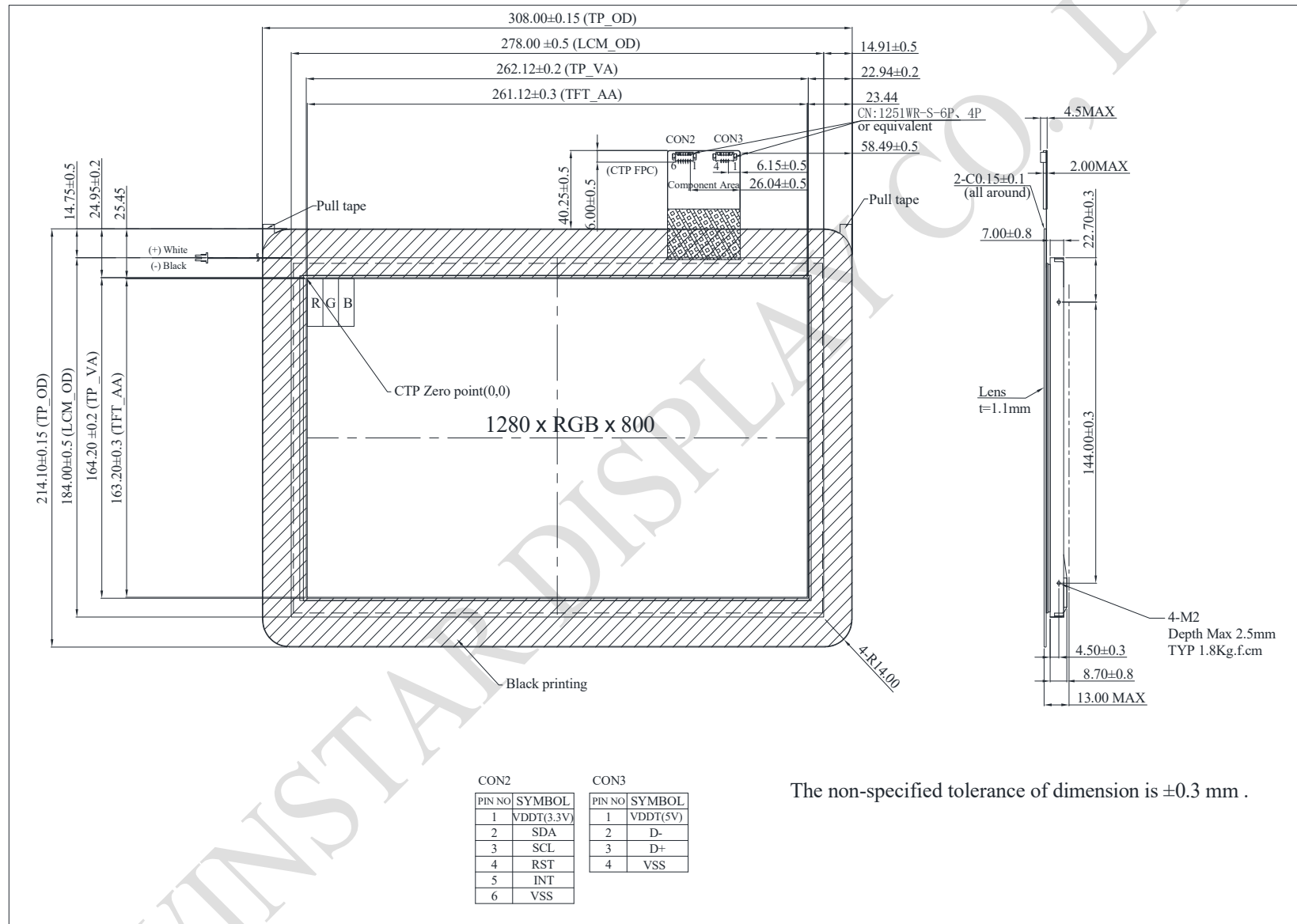
2. CON2

Pin No.	Symbol	Function
1	VDDT(3.3V)	Power supply
2	SDA	I2C data input and output
3	SCL	I2C clock input I2C clock input
4	RST	External Reset, Low is active
5	INT	External interrupt to the host
6	VSS	Ground for analog circuit

3. CON3

Pin No.	Symbol	Function
1	VDDT(5V)	Power supply
2	D-	Data -
3	D+	Data +
4	VSS	System ground

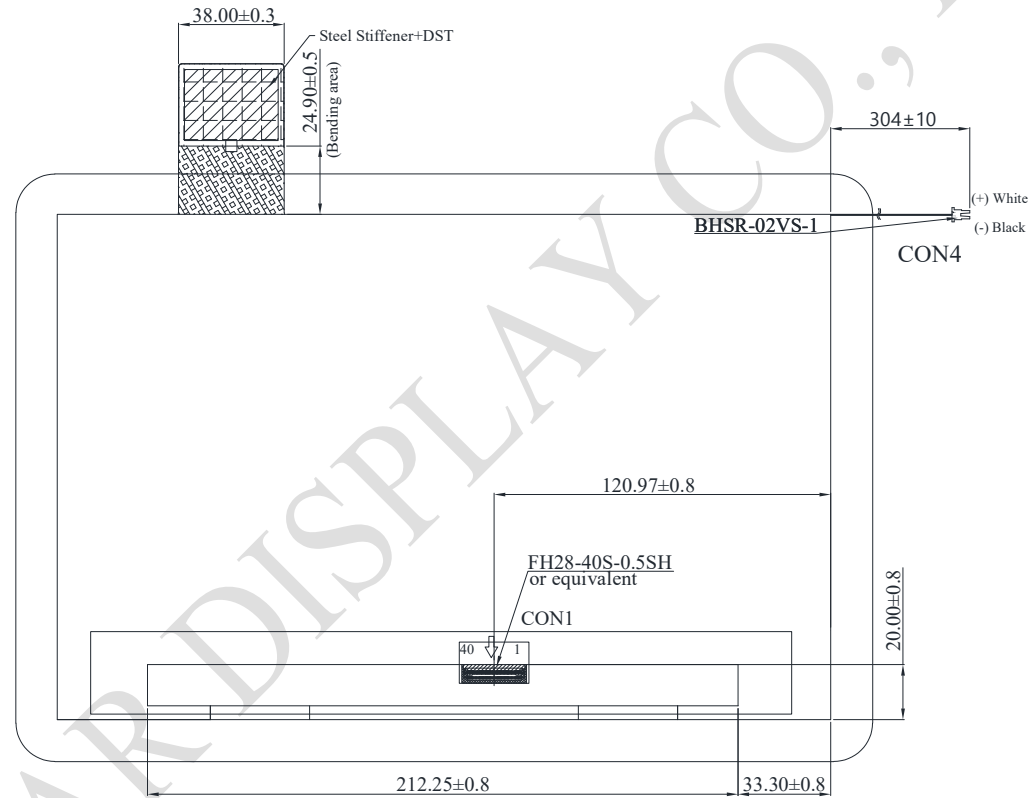
Contour Drawing



The non-specified tolerance of dimension is ±0.3 mm .

CON1	
PIN NO	SYMBOL
1	NC
2	VDD
3	VDD
4	VDD
5	NC
6	GND
7	GND
8	RIN0-
9	RIN0+
10	GND
11	RIN1-
12	RIN1+
13	GND
14	RIN2-
15	RIN2+
16	GND
17	LVDS_CLK-
18	LVDS_CLK+
19	GND
20	RIN3-
21	RIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	NC
28	GND
29	NC
30	NC
31	GND
32	GND
33	GND
34	NC
35	NC
36	NC
37	NC
38	NC
39	NC
40	NC

CON4	
PIN NO	SYMBOL
1	White(VLED+)
2	Black(VLED-)



The non-specified tolerance of dimension is ± 0.3 mm .